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Application Number	10/720,857
Filing Date	November 24, 2003
First Named Inventor	Pravas Pradhan et al.
Group Art Unit	2816
Examiner Name	Not yet assigned
Attorney Docket Number	112055-0073U

[illegible][illegible]

Date Considered

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08A (08-00)

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Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
n	7.	SEEVINCK, Evert, Current-Mode Techniques for High-Speed VLSI Circuits with to Current Sense Amplifier for CMOS SRAM's, IEEE Journal of Solid-State Circuits, Vol. 26, No. 4, April 1991	
n	8.	SIM, Jae-Yoon, A 1-Gb/s Bidirectional I/O Buffer Using the Current-Mode Scheme, IEEE Journal of Solid-State Circuits, Vol. 34, No. 4, April 1999	

Examiner Signature	Date Considered	12/7/04
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